

# LMR38010S-Q1 PSPICE Transient Model Features and Limitations

\* Model Usage Notes:

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\* 1. The following features have been modeled

\* a.Startup

\* b.Adjustable Output Voltage

\* c.Enable

\* d.Switching Frequency(RT)

\* e.Power-Good Flag Output

\* f.Minimum ON-Time, Minimum OFF-Time and Frequency Foldback

\* g.Bootstrap Voltage

\* h.Over Current and Short Circuit Protection

\* i.Soft Start

\* j.Device Functional Modes(FPWM FIXED DEVICE)

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\* 2. Features haven't been modelled

\* a. Thermal Shutdown

\* b. Steady state

\* c. Sync

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\* 3. Ground Pins have been tied to 0V internally and hence model does not support Inverting topologies.